

### **Key Features & Benefits**

- RoHS compliant for all six substances
- Universal input voltage range (36-75 VDC)
- High power density, 15.15 Watts/cubic inch
- 1U or 2U height configurations
- Active current share with ORing FETs
- I2C interface status and control
- External standby voltage of 5 VDC @ 2 A
- Overtemperature, overload, and overvoltage protection
- Status LEDs: INPUT OK, DC OK, Overtemperature
- · Airflow direction from rear-to-front

#### **Applications**

- Telecom,
- Datacom
- Distributed Power Systems

# FND850-12RG Power Supply

## DC Input, 12V Output, 850 W

The FND850-12RG DC-DC front-end provides a 12 VDC output for telecom, datacom, and other distributed power applications. The FND850-12RG small 1U by 2U size allows for configurations of either height in hot-swap redundant systems while its internal fan and cooling design permits wide use with reliable operation.

Status is provided with front panel LEDs, logic signals, and via the I<sup>2</sup>C management interface bus. In addition, the I<sup>2</sup>C bus can enable the power supply, control fan speed, and adjust the output voltage from 7 to 12 VDC. This powerful feature allows the same power supply to be used in various applications.

In addition, the FND850-12RG is designed with airflow from the rear of the power supply to the front. This airflow direction supports those critical applications where space limitations and/or higher ambient temperatures near the rear of the racking system, prohibit the discharge of higher temperature airflow from regular front-to-rear cooled power supplies.

The FND850-12RG meets international safety requirements and is CE marked to the Low Voltage Directive (LVD).

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#### **Model Selection**

MODEL	INPUT VOLTAGE (VDC)	NOMINAL OUTPUT VOLTAGE (VDC)	ADJUSTMENT RANGE (VDC)	MAXIMUM OUTPUT CURRENT (Amps)	LINE REGULATION (%)	LOAD REGULATION (%) <sup>1</sup>	RIPPLE & NOISE pk-pk % 2
	36 to 44	12	7 to 12	58	±0.17	±0.8	1
FND850-12RG	44 to 75	12	7 to 12	71	±0.17	±0.8	1
	36 to 75	5 (Standby)	N/A	2	±5	±5	1

<sup>&</sup>lt;sup>1</sup> % of Vnom

## **Input Specifications**

PARAMETER	CONDITIONS / DESCRIPTION	MIN	NOM	MAX	UNITS
Input Voltage	DC input voltage	36		75	VDC
Hold-up Time	After DC input is removed at full power	4	5		ms
Input Current	At 100% load (main and standby output) , $Vin = 36\ VDC$			23.5	A rms
Inwich Cure Curent	Internally limited. Vin = 36 VDC @ 25 °C			35	A mle
Inrush Surge Current	Internally limited. Vin = 75 VDC @ 25 °C			25	A pk

### **Output Specifications**

PARAMETER	CONDITIONS / DESCRIPTION	MIN	NOM	MAX	UNITS
Efficiency	Full rated load at Vin = 36 to 75 VDC, Vout = 12V	84	88		%
Emolericy	Full rated load at Vin = 36 to 75 VDC, Vout = 7V	80	82		70
Minimum Load	Minimum loading required to maintain regulation.	0			Α
Output Current	Main output: Vin = 36 to 44 VDC Main output: Vin = 44 to 75 VDC		58 71		A rms
Nominal Output Power	12V output, Vin = 36 to 44 VDC 12V output, Vin = 44 to 75 VDC Standby output Vin = 36 to 75 VDC			700 850 10	W
Overshoot	Output voltage overshoot at turn-on.			3	%
Transient Response	Maximum recovery time and deviation of initial set point due to a 50% load change, 1A/µs.  Maximum recovery time: 12V output Maximum deviation: 12V output			1200 2	μs %
Transient nesponse	Maximum recovery time and deviation of initial set point due to a 100% load change, 1Α/μs.				
	Maximum recovery time: 12V output Maximum deviation: 12V output			2 4	ms %
Turn-On Delay	Time required for initial output voltage stabilization after application of DC input.			2	Sec
Output Regulation	See Model Selection table.				



<sup>&</sup>lt;sup>2</sup> Main 12 V output has built-in droop regulation.

## **Interface Signals and Internal Protection**

PARAMETER	CONDITIONS /	DESCRIPTION	MIN.	NOM.	MAX.	UNITS
Overvoltage Protection	Latch-style over	voltage protection. Output adjusted to 12V: Output adjusted to 7V:	14.36 8.8		15.65 9.2	V
Overcurrent Protection	Current limit.	12V output (Vin = 44 to 75V) 12V output (Vin = 36 to 44V) Standby output:	74 61 2.2	78 64	85 70 4	Α
Short-Circuit Protection	Power supply to	recover when short is removed.				
Overtemperature/ Fan Failure Warning	condition or bloc Power supply wi Amber OT LED v OT/Fan Fail is ar 20-mA pull-down condition. Outp	hut down in the event of an overtemperature cked fan rotor. Supply's fan and Vaux are active. Il recover when OT condition is removed. vill turn ON to indicate fault condition.  In open-collector signal with the in. High signal indicates a normal operating out will go low at least 100 ms before OT condition power supply. An amber light will indicate FF				
DC OK	Represents perc Below this voltage	tor signal with active 20-mA pull-down. ent of output voltage where signal is OK. ge, a signal high condition indicates an output blerance. Green DC OK LED on front panel operation. 3	90		110	%
Input Power Fail Warning	indicates an inpugreen INPUT OK	tor signal with active 20-mA pull-down. High at power fail. Power Fail Warning will turn OFF LED. Represents the time after warning signal as to 95% due to loss of input power. 3	4			ms
Power Supply Present Signal	determine if pow	nnection to logic ground which allows user to er supply is present. <sup>3</sup>		10		Ω
Current Share	Main output only $(I\Delta < 5\% \text{ Isum_n})$	v. Difference in current between two supplies om)			7.1	Α
Remote Sense	Total voltage cor main output.	mpensation for cable losses with respect to the			0.5	V
Output Enable	Open circuit or lo Vo1; Vaux and fa capability) or jun voltage on enabl	ogic high from ENA pin to Vo1 RTN shuts OFF an are operational. Logic low (2 mA sink oper will turn ON Vo1 within 100 ms. Open circuit e pin is 3.3 VDC. Externally applied voltage to could NOT exceed 7 VDC. Signal is referred to as ITN).				

<sup>&</sup>lt;sup>3</sup> Also available on I<sup>2</sup>C data line.

## I<sup>2</sup>C Bus Management Interface <sup>4</sup>

Static	Includes static information such as: part number and revision leve manufacturing location.	el, output rating, serial number, date code	, and
Status (Logic 1 or 0)	Power Supply OK. Input OK. DC Output OK. Power Supply Seated. Overtemperature. Overcurrent. Fan OK.		
Real-Time Monitoring	Output voltage (main output) 0.01V LSD. Output current (main output) 0.1A LSD.		
I <sup>2</sup> C monitor signal accuracy at 25°C	Output voltage measurement (main output) Output current measurement (main output)	± 0.05 ± 1	V A
Control Signals (Logic 1 or 0)	Enable for main output. Fan speed level.		
I <sup>2</sup> C setting	Output voltage setting resolution (main output) Output current setting resolution (main output)	0.0154 0.5	V A

<sup>&</sup>lt;sup>4</sup> Reference "I<sup>2</sup>C Management Interface" and "EEPROM Table of Contents" documents for FND850-12RD (consult factory).



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## Safety, Regulatory, and EMI Specifications

PARAMETER	CONDITIONS / DESCRIPTION		MIN	NOM	MAX	UNITS
Agency Approvals	UL60950, (UL) CSA 60950 (cUL), E LVD.	EN60950 (TÜV), CE Mark for				
Electromagnetic Interference	FCC CFR title 47 Part 15 Sub-Part	B, EN55022/ CISPR 22 Conducted: Radiated:	Level B Level A			
Voltage Fluctuation	Unit must start up		Pass			
ECD Cussentability	Per EN61000-4-2, Level 4. contact	t	8			kV
ESD Susceptability	Per EN61000-4-2, Level 4. air		15			kV
Radiated Susceptability	Per EN 61000-4-3, Level 3.		10			V/M
EFT/Burst	Per EN 61000-4-4, Level 4.		± 2			kV
Input Transient Protection	Per EN 61000-4-5, Class	Line-to-Line: Line-to-Ground:	± 0.5 ± 0.5			kV
RF Conducted Disturbances	Per EN 61000-4-6, Level 3. $^{\rm 5}$		10			V
Leakage Current	Per EN60950.	At 36 VDC:			3.5	mA

<sup>&</sup>lt;sup>5</sup> RF Conducted disturbances value to be provided.

## **Environmental Specifications**

PARAMETER	CONDITIONS / DESCRIPTION		MIN	NOM	MAX	UNITS
Altitude	Operating. Non-Operating.				10K 40K	ASL ft
Operating Temperature	Internal DC fan for cooling.	At 100% load: <sup>6</sup> At 50% load:	0 0		50 70	°C
Storage Temperature			-40		85	°C
Temperature Coefficient	0 °C to 70 °C (after 15-minute warm-up).				0.02	%/°C
Relative Humidity	Non-condensing (operational)				90	%RH
Relative numicity	Non-condensing (non-operational)				95	%RH
Shock	Operating: half-sine, 10 ms, 3-axis.  Non-Operating: half-sine, 10 ms, 3-axis.				+20 +40	Gpk
Vibration	Operating: swept sine 5-2000-5 Hz, 5-32 Hz, 0 Non-operating: random 10-2000 Hz.	0.02îDA, 32-2000 Hz.			1 6.16	Gpk Grms

<sup>&</sup>lt;sup>6</sup> At temperature over 50°C – linear power derating to 50% of rated load up to 70°C

#### Reliability

PARAMETER	CONDITIONS / DESCRIPTION	MIN NOM	MAX	UNITS
MTBF	(Calculated) MILHDBK 217F Ground Benign.	100 000		hrs
WIIDI	Useful Life.	7		yrs



#### **Mechanical**

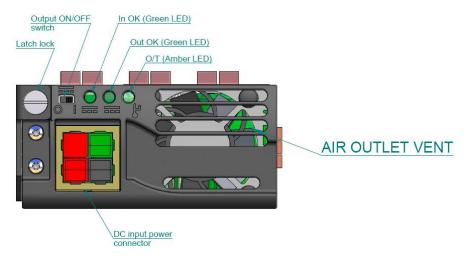


Figure 1 - Front View

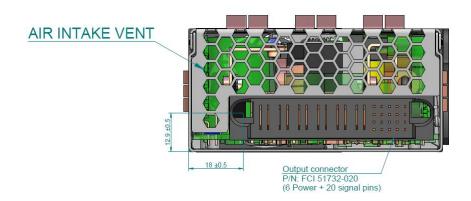


Figure 2 - Rear View

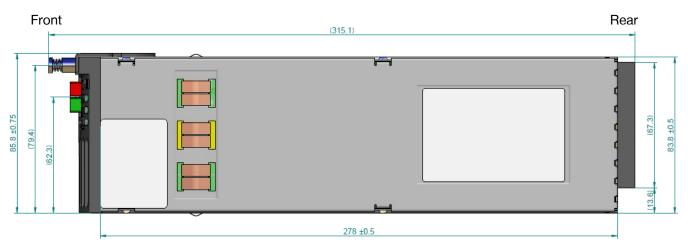


Figure 3 - Top View



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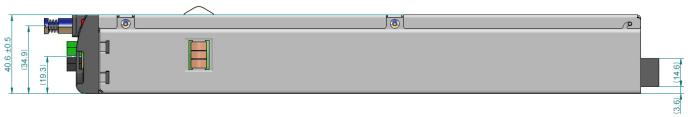


Figure 4 - FND850-12DRG Side View

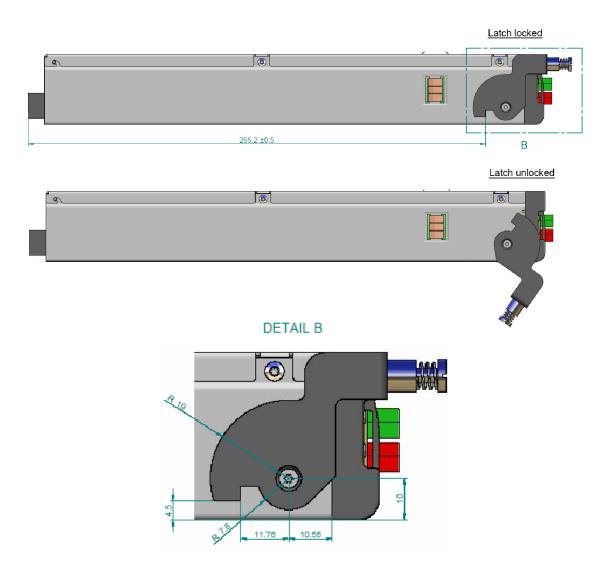


Figure 5 - FND850-12RG locking system



#### **Connector Information**

#### **Power Supply:**

Input - Anderson Power Products (APP): PP45 Powerpole Connector family Output - FCI PoweBlade family connector: P/N 51732-020LF

#### **Mating Connections:**

Input - Anderson Power connector: PP45 Powerpole Connector family

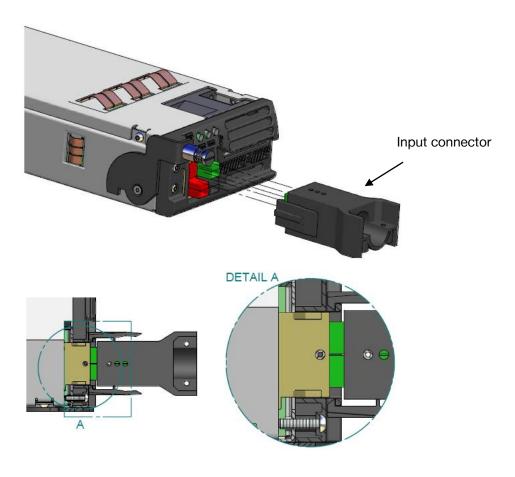
45A crimp contact AWG# 10/14 3x: P/N 261G2-LPBK

Flexible Conduit Hardware Pak 1x: P/N 110G10

Housing red 1x: P/N 1327 Housing green 1x: P/N 1327G5 Housing black 1x: P/N 1327G6 Red long spacer 1x: P/N 1399G2

Output - FCI PoweBlade family connector: P/N: 51742-10602000AALF (Backplane - straight pins)

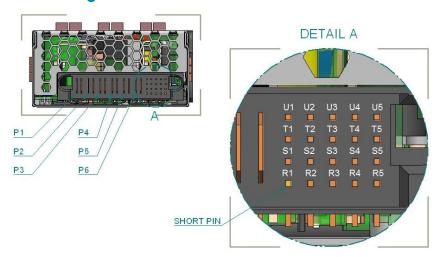
FCI PoweBlade family connector: P/N: 51762-10602000AALF (Right Angle)



INPUT	LOCATION
Chassis (Safety/EMI) Ground	Green
+ DC input voltage	Red
- DC input voltage	Black



#### **Output Connector Pin Assignments**



#### FND850-12RG Signal/Pin/Ground Reference Information

SIGNAL	PIN LOCATION	GROUND REFERENCE
OverTemperature / Fan Fail	U1	Logic Ground (LRTN)
DC Input Fail Warning	U2	Logic Ground (LRTN)
Power Supply Present	U3	Logic Ground (LRTN)
Output Voltage Fault	U4	Logic Ground (LRTN)
Internal Ground	U5	Internal Ground (SRTN1)
ADDR0, I <sup>2</sup> C Address Bus	T1	Internal Ground (SRTN)
ADDR1, I <sup>2</sup> C Address Bus	T2	Internal Ground (SRTN)
ADDR2, I <sup>2</sup> C Address Bus	T3	Internal Ground (SRTN)
ADDR3, I <sup>2</sup> C Address Bus	T4	Internal Ground (SRTN)
ADDR4, I <sup>2</sup> C Address Bus	T5	Internal Ground (SRTN)
DATA, I <sup>2</sup> C Data Line	S1	Logic Ground (LRTN)
CLOCK, I <sup>2</sup> C Clock Line	S2	Logic Ground (LRTN)
Auxiliary Power +5V	S3	Aux Ground
Auxiliary Power Ground	S4	Aux Ground
Logic Ground	<b>S</b> 5	Logic Ground (LRTN²)
Output Enable <sup>3</sup>	R1	Logic Ground (LRTN)
Vsense+	R2	Vsense-
Vsense-	R3	Vsense-
Output Margin	R4	Internal Ground (SRTN)
Active Current Sharing	R5	Internal Ground (SRTN)
Vout+	P1, P2, P3	Vsense-
Vout-	P4, P5, P6	Vsense-

## For more information on these products consult: tech.support@psbel.com

**NUCLEAR AND MEDICAL APPLICATIONS** - Products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

**TECHNICAL REVISIONS** - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.

<sup>&</sup>lt;sup>3</sup> Short pin length



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<sup>&</sup>lt;sup>1</sup> SRTN (Signal Return) is internally connected with Vout-

<sup>&</sup>lt;sup>2</sup> LRTN (Logic Return) 10R resistor internal connected to Aux Ground