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ELECTRICAL CHARACTERISTICS @25°C

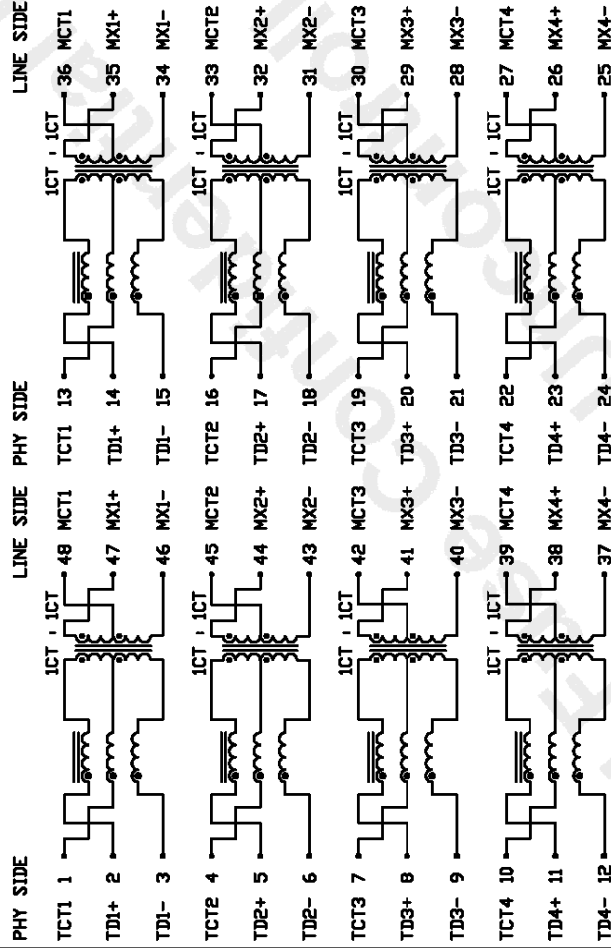


URNS RATIO
 PHY : LINE 1CT : 1CT ±2%
POLARITY
 PER DOT CONVENTION
OCV
 @100kHz, 100mV
 350uH MIN WITH 11mA DC BIAS
 120uH MIN WITH 19mA DC BIAS
DC RESISTANCE (1/2 WINDING)
 0.35 OHM MAX
INTERWINDING CAPACITANCE, C_{WW}
 @1MHz, 20mV
 30pF MAX
INSERTION LOSS
 0.3MHz - 1MHz 1.1dB MAX
 1MHz - 100MHz 1.0dB MAX
 100MHz - 125MHz 1.2dB MAX
RETURN LOSS @100OHM
 0.5MHz - 40MHz 18dB MIN
 40MHz - 100MHz 12-20LOG(f/80MHz)dB MIN
CROSSTALK
 0.3MHz - 100MHz 33-20LOG(f/100MHz)dB MIN
CM TO CM REJ
 0.3MHz - 100MHz 30dB MIN
CM TO DM REJ
 0.3MHz - 100MHz 35dB MIN
BALANCED DC LINE CURRENT
 600mA MAX @57VDC CONTINUOUS
HIPOT
 LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

SCHEMATIC

PORT 1

PORT 2



ORIGINATED BY	DATE
Alice Pang	2020-06-29
DRAWN BY	DATE
ZC Guo	2020-06-29

TITLE	PART NO. / DRAWING NO.
ELECTRICAL SPECIFICATION	S558-5500-72
	FILE NAME
	S558550072.DWG

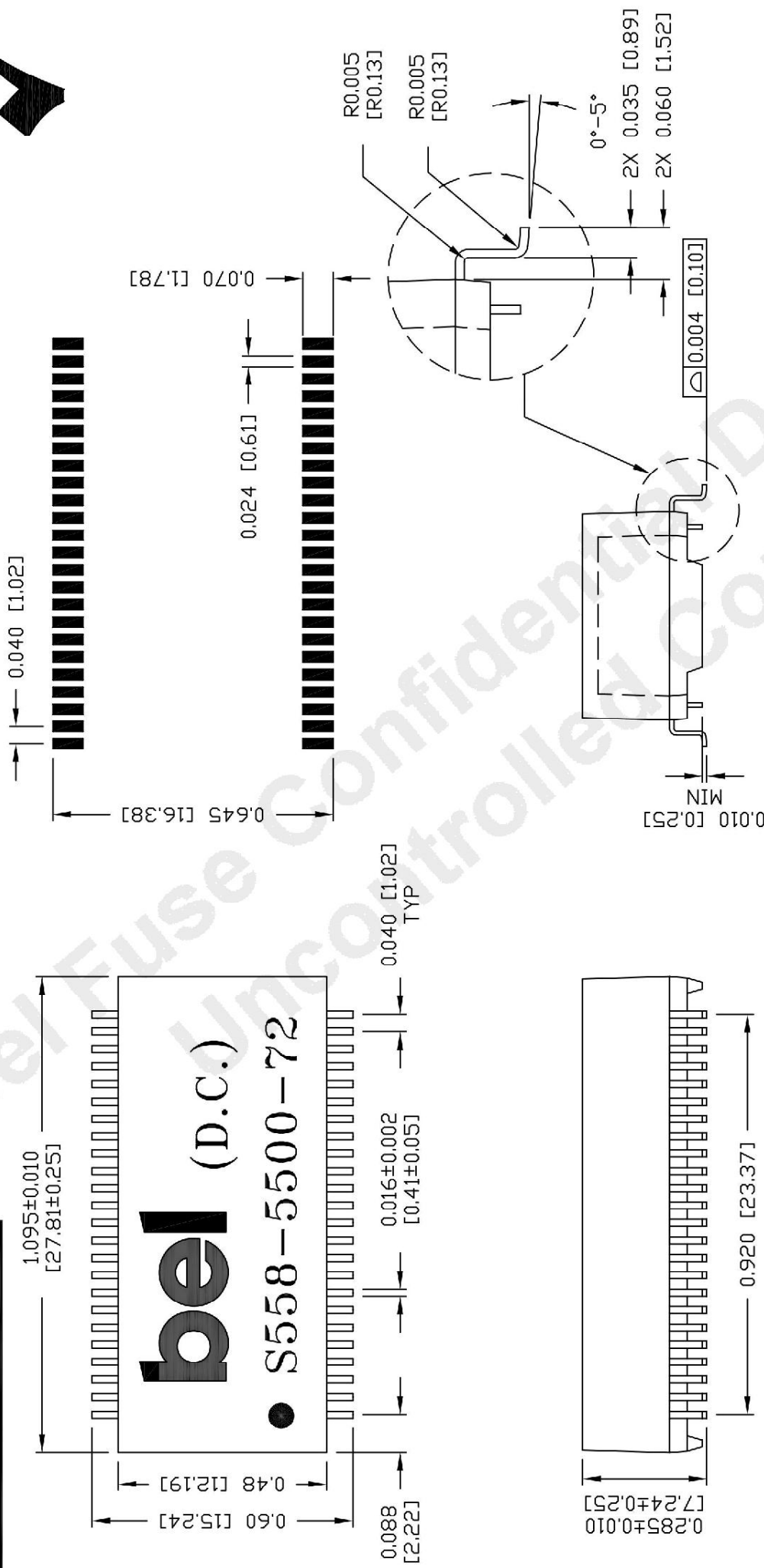
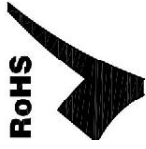
STANDARD DIM.	[] METRIC DIM. AS REF.
TOL. IN INCH	UNIT : INCH [mm]
.X	SCALE : N/A
.XX	SIZE : A4
.XXX	

REV. : D PAGE : 2



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SUGGESTED PCB PAD LAYOUT



NOTES:

- STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- PACKAGE CODE: "QVS001".

ORIGINATED BY: Lawrence Tsang DATE: 2015-10-29		TITLE: MECHANICAL OUTLINE		PART NO. / DRAWING NO.: S558-5500-72		STANDARD DIM. [] METRIC DIM. AS REF.	
DRAWN BY: ZC Guo DATE: 2015-10-29		FILE NAME: S558550072C.DWG		TOL. IN INCH: .X ±0.01 .XX ±0.005 .XXX ±0.005		UNIT: INCH [mm] SCALE: N/A SIZE: A4	
REV.: C				PAGE: 3			

